



PTO/SB/08a/b (08-03)
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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Complete if Known		
			Application Number	09/659,767-Conf. #4989	
			Filing Date	September 11, 2000	
			First Named Inventor	Hiroyuki Ishii	
			Art Unit	2631	
			Examiner Name	J. B. Corrielus	
Sheet	1	of	1	Attorney Docket Number	G0126.0189/P189

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U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
OK	BA	JP 62-22141 (w/English abstract)	01/30/1987	_____	_____	
OK	BB	JP 10-267972 (with English abstract)	10/09/1998	_____	_____	
OK	BC	JP 5-218914 (with English abstract)	08/27/1993	_____	_____	
OK	BD	JP 10-23086 (with English abstract)	01/23/1998	_____	_____	
OK	BE	JP 2001-111644	04/20/2001	_____	_____	
OK	BF	JP 7-7678	01/10/1995	_____	_____	
OK	BG	JP 62-128247	06/10/1987	_____	_____	
OK	BH	JP 2000-286908 (with English abstract)	10/13/2000	_____	_____	
OK	BI	JP 2-73749	03/13/1990	_____	_____	

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
OK	CA	Taira et al., "Automatic Classification of Modulation Types, Including Analogue Types", Technical Report of IEICE, Dept. of Electrical Engineering, Faculty of Science and Technology, Keio University, Japan, pgs. 29-36 (1998)	
OK	CB	Taira et al., "Modulation Type Classification Based on the Block Demodulation Method", Technical Report of IEICE, Dept. of Electrical Engineering, Faculty of Science and Technology, Keio University, Japan, pgs. 47-54 (1997)	
OK	CC	Taira et al., "Block Demodulation Method Coping with Modulation Type Variation", Technical Report of IEICE, Dept. of Electrical Engineering, Faculty of Science and Technology, Keio University, Japan, pgs. 7-14 (1997)	
OK	CD	Copy of Japanese Office Action dated February 25, 2004 (and English translation of relevant portion)	

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Jim Corrielus

6/2/04



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**INFORMATION DISCLOSURE
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		Number-Kind Code ² (if known)			
MA	AA	US-5,912,922	06-15-1999	Koszarsky et al.	

FOREIGN PATENT DOCUMENTS

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MA	CA	Dubuc et al., "An Automatic Modulation Recognition Algorithm for Spectrum Monitoring Applications", Communications Research Center, IEEE International Conference on Communications. Conference Record. Vancouver, CA, pgs. 570-574 (June 1999)	
MA	CB	Portillo-Garcia et al., "A Microcomputer-based General Architecture for Radiocommunication Signal Classification and Digital Demodulation", Proceedings of the European Signal Processing Conference, Barcelona, Spain, pgs. 1919-1922 (September 1990)	

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MA Corrielus 6/2/04